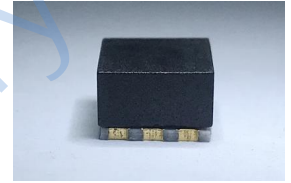


Power Splitter/Combiner

HT-LRPS-2-25J



Features

- low insertion loss, 0.8 dB typ.
- good isolation, 20 dB typ.
- aqueous washable
- J-leads for strain relief and excellent solderability

Applications

- cellular
- defense & federal communications

Transformer Electrical Specifications

Freq. range (MHz)	Isolation(dB)		Insertion Loss (dB) Above 3.0 dB.		Phase Unbalance (Degrees) Max.	Amplitude Unbalance (dB) Max.
	min	max	min	max		
1700-2500	16	20	0.8	1.3	10	0.9

2 Way-0° 50Ω 1700 to 2500 MHz

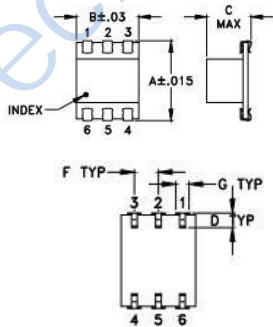
electrical schematic



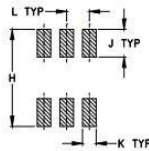
Typical Performance Data (TEST CONDITIONS: INPUT POWER = 0dBm @Temperature = +25°C)

Frequency. (MHz)	Total Loss (dB)		Amp. Unbal. (dB)	Isolation (dB)	PhaseUnbal. (deg.)	VSWRS		
	S-1	S-2				S	1	2
1700	3.55	3.45	0.10	17.17	1.63	1.43	1.14	1.15
1732	3.55	3.45	0.10	17.52	1.55	1.40	1.13	1.16
1764	3.54	3.45	0.09	17.89	1.53	1.38	1.13	1.17
1828	3.53	3.46	0.07	18.73	1.43	1.35	1.12	1.19
1892	3.52	3.46	0.07	19.69	1.24	1.31	1.12	1.21
1956	3.52	3.47	0.05	20.83	1.13	1.28	1.12	1.25
2020	3.53	3.48	0.05	22.18	0.94	1.26	1.12	1.27
2084	3.53	3.49	0.04	23.91	0.75	1.25	1.11	1.30
2148	3.55	3.51	0.04	26.14	0.50	1.25	1.12	1.33
2212	3.56	3.54	0.02	29.31	0.34	1.25	1.12	1.36
2276	3.59	3.57	0.03	34.50	0.11	1.30	1.13	1.40
2340	3.63	3.61	0.03	40.88	0.21	1.32	1.14	1.43
2404	3.68	3.65	0.03	34.00	0.40	1.38	1.15	1.47
2468	3.74	3.70	0.03	28.52	0.79	1.44	1.16	1.51
2500	3.77	3.73	0.03	26.61	0.93	1.49	1.17	1.53

Outline Drawing



PCB Land Pattern



Suggested Layout, Tolerance to be within ±002

Pin Connections	
SUM PORT	6 (input)
PORT 1	4 (output1)
PORT 2	3 (output2)
NOT USED	1,2,5

Maximum Ratings

Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Power Input (as a splitter)	1 W max.
Internal Dissipation	0.125W max.

Permanent damage may occur if any of these limits are exceeded.

Outline Dimensions: Unit (mm)

A	9.91	J	3.05
B	7.87	K	1.52
C	5.72	L	2.54
D	1.52	M	-
E	-		
F	2.54		
G	1.14		
H	10.67		
WT		0.50g	

